

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S63	5	((switch\$3 chang\$3) with (context state) with (cpu processor)) same ((context state) with (stor\$3 sav\$3) with privileg\$3 with register)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/09/01 17:55
S62	0	((switch\$3 chang\$3) with (context state) with (cpu processor)) same ((context state) with (stor\$3 sav\$3) with priviledg\$3 with register)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/09/01 17:55
S56	7	((switch\$3 chang\$3) with (context state) with (cpu processor)) same ((context state) with (stor\$3 sav\$3) with shadow\$3 with register)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/09/01 17:27
S22	666	((switch\$3 chang\$3) with (context state) with (cpu processor)) same ((context state) with (stor\$3 sav\$3) with register)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/03/28 17:30
S60	60	S59 not S57	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/03/28 16:50
S59	67	((switch\$3 chang\$3) with (context state) with (cpu processor)) and ((stor\$3 sav\$3) with (context state) with (shadow\$3 near3 register))	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/03/28 16:50
S58	1	S57 not S56	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/03/28 16:44
S57	8	((switch\$3 chang\$3) with (context state) with (cpu processor)) same ((context state) with (stor\$3 sav\$3)) same (shadow\$3 near3 register)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/03/28 16:43
S55	4	inconsequential near3 register	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/03/28 16:26
S7	1	S5 and (nmi)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/08/05 10:45